

# IEEE COMPUTER SOCIETY ANNUAL SYMPOSIUM ON VLSI

# ISVLSI 2025

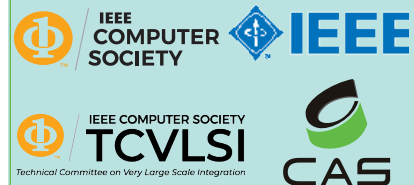
[www.ieee-isvlsi.org](http://www.ieee-isvlsi.org)

July 6 -9, 2025  
Kalamata, Greece



The Symposium explores emerging trends and novel ideas and concepts covering a broad range of topics in the area of VLSI: from VLSI circuits, systems and design methods, to system level design and system-on-chip issues, to bringing VLSI methods to new areas and technologies like nano- and molecular devices, hardware security, etc. ISVLSI 2023 highlights the theme of Internet-of-Things. Future design methodologies are also one of the key topics at the Symposium, as well as new EDA tools to support them. Over three decades the Symposium has been a unique forum promoting multidisciplinary research and new visionary approaches in the area of VLSI, bringing together leading scientists and researchers from academia and industry.

The Symposium Proceedings are published by IEEE Computer Society Press. Papers from ISVLSI editions have been subsequently published in special issues of top archival journals which indicates the general high quality of the papers. The Symposium has established a reputation in bringing together well-known international scientists as invited speakers; the emphasis on high quality will continue at this and future editions of the symposium.



#### General Chairs

Nikolaos Voros, University of Peloponnese, Greece

Michael Huebner, Brandenburgische Technische Universität Cottbus-Senftenberg, Germany

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Jürgen Becker, Karlsruhe Institute of Technology, Germany

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Fernanda Kastensmidt, UFRGS, Brazil

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#### Local Arrangement Chair

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OBS: Full Committees are in the webpage

## Contributions are sought in the following areas

Emerging Trends in VLSI, Nanoelectronics, Molecular, Biological and Quantum Computing, VLSI Circuits and Systems, FPGA Based and Reconfigurable Systems, System Design and Security, SoC and Noc Design, ManyCore/Multicore Designs, ASICs, System Issues in Complexity, Low Power, Heat Dissipation, Power Awareness in VLSI Design, Test and Verification, Mixed-Signal Design and Analysis, EDA, Logic and Behavioral Synthesis, Physical Design. (please, see the webpage for more topics)

The Symposium Program will include contributed papers and invited speakers, and also a poster session. Keynote addresses, special sessions and Student Research Forum are planned as well. Authors are invited to submit full-length, original, unpublished papers. To enable blind review, the author list should be omitted from the main document. All submissions should be in PDF format.

## Paper submission deadlines

**Submission Deadline: February 10, 2025**

**Acceptance Notification: May 10, 2025**

**Submission of Final Version: May 30, 2025**

Selected papers from ISVLSI 2025 will be invited for submission to a journal special issue. The selection process is based on reviewer feedback and quality of conference presentation

