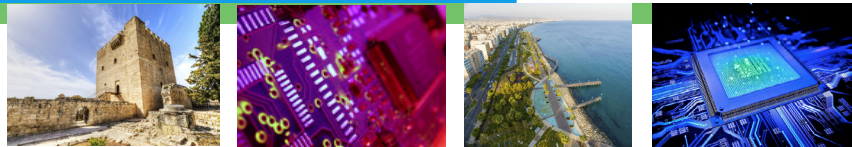


IEEE COMPUTER SOCIETY ANNUAL SYMPOSIUM ON VLSI



CALL FOR PAPERS *European Projects Special Session*

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The ISVLSI 2020 symposium will include an extra section dedicated to European funded Projects. Coordinators of EU funded projects addressing ISVLSI relevant topics are invited to submit a paper describing the recent accomplishments, preliminary ideas, or work in progress scenarios of the projects. Papers will be peer-reviewed as regular ISVLSI submissions and included in the Proceedings as regular papers. The submissions should follow the same format and page limit (6-pages) of the conference. During the submission, the “Research Projects” track should be selected.

For additional information, please contact

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Paper submission deadlines

Submissions Deadline: ~~February 26, 2020~~ **March 27, 2020**

Acceptance Notification: April 20, 2020

Submission of Final Version: May 10, 2020

