IEEE COMPUTER SOCIETY ANNUAL SYMPOSIUM ON VLSI

ISVLSI 2020

CALL FOR PAPERS

www.isvlsi.org

July 6-8, 2020
Limassol, Cyprus

ISVLSI 2020 explores emerging trends and novel ideas and concepts in the area of VLSI. The symposium covers a range of topics: from VLSI circuits, systems and design methods to system-level design and system-on-chip issues, to bringing VLSI experience to new areas and technologies such as security, artificial intelligence and cyber-physical systems. The symposium will also emphasize future design methodologies and new CAD tools to support them as key topics. Over three decades ISVLSI has been a unique forum promoting multidisciplinary research and new visionary approaches in the area of VLSI, bringing together leading scientists and researchers from academia and industry. The ISVLSI proceedings will be published by IEEE-CS Conference Publishing Servies (CPS). Selected papers from past editions have been subsequently published in special issues of top archival journals. ISVLSI has a good reputation of bringing together well-known international scientists as invited speaks. ISVLSI 2020 will continue the momentum and carry forward these well-established trends for further growth of the symposium.

Contributions are sought in the following areas

1) Circuits, Reliability, and Fault-Tolerance
2) Computer-Aided Design and Verification
3) Digital Circuits and FPGA based Designs
4) Emerging and Post-CMOS Technologies
5) System Design and Security
6) VLSI for Applied and Future Computing

OBS: more details about sub-topics in the conference webpage.

New attraction in ISVLSI 2020: Quantum Computing Workshop (QCW)

The Symposium Program will include contributed papers and speakers invited by the Program Committee as well as a poster session. Keynotes, panels, special sessions, research demo sessions (RDS), and a Student Research Forum (SRF) are planned as well. Taking place in Europe, the Symposium will also include sessions and forums dedicated to European research projects. Authors are invited to submit full-length, original, unpublished manuscripts in PDF format (up to 6 pages), omitting author information details.

Paper submission deadlines

Submissions Deadline: ~~February 20, 2020~~ **March 27, 2020**
Acceptance Notification: April 20, 2020
Submission of Final Version: May 10, 2020

Selected papers from ISVLSI 2020 will be invited for submission to a journal special issue.The selection process is based on reviewer feedback and quality of conference presentation

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